

	STM32L1	STM32L4
	typedef struct { ... __IO uint32_t CALIBR ; ... uint32_t RESERVED7; ... } RTC_TypeDef;	typedef struct { ... uint32_t reserved; ... __IO uint32_t OR ; ... } RTC_TypeDef;
RTC	Digital calibration (DC) is configured through bits DC[4:0] of RTC_CALIBR register. This number ranges from 0 to 31 corresponding to a time interval (2xDC) ranging from 0 to 62.	RTC_CALIBR register is not available
	RTC option register (RTC_OR) is not available.	RTC_OR controls <ul style="list-style-type: none"> • remap of the RTC outputs (RTC_OUT) on PB2 • RTC_ALARM on PC13 output type
	The Calibration output (COE) bit in RTC_CR enables the RTC_CALIB output.	The COE bit is not available in RTC_CR.
	RTC tamper and alternate function configuration register (RTC_TAFCR)	RTC tamper configuration register (RTC_TAMPCR). Bit ALARMOUTTYPE available in RTC_OR register